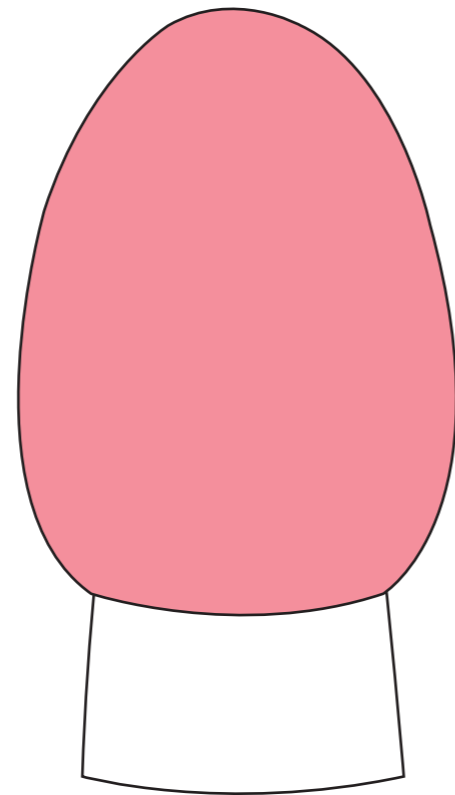
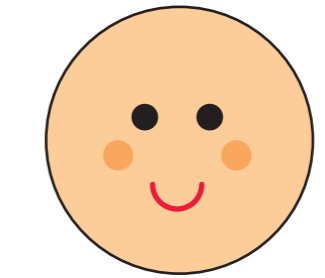
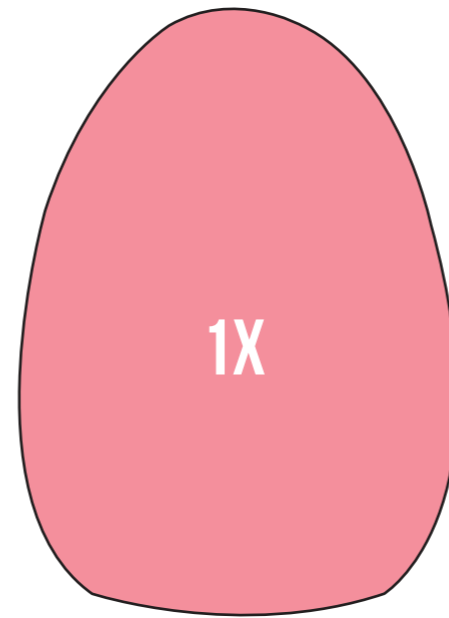


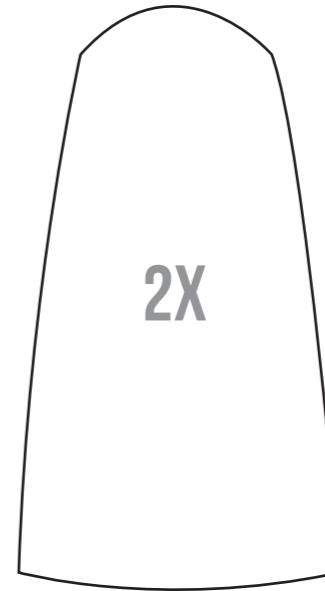
~
FRENTE



~
VERSO



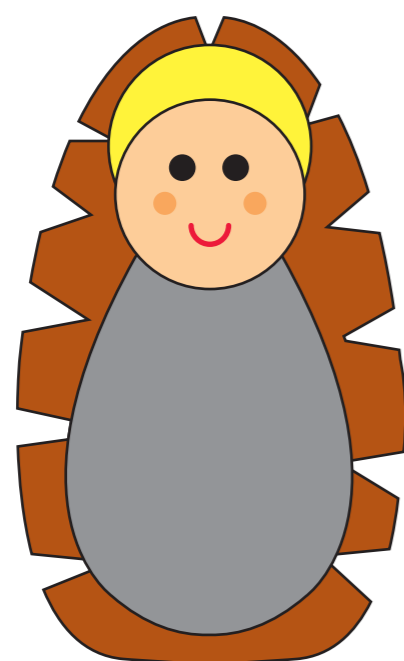
} 1X



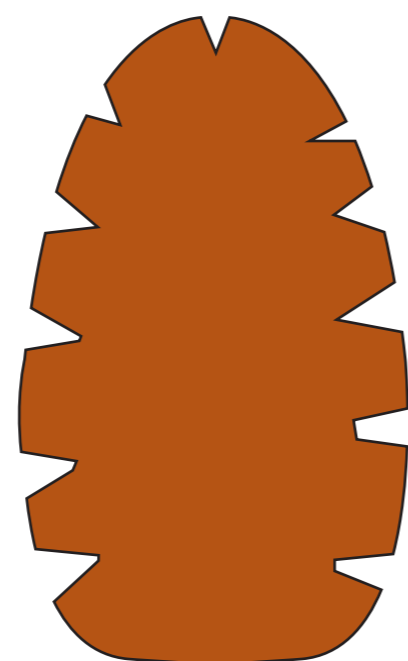
2X

} + } 2X

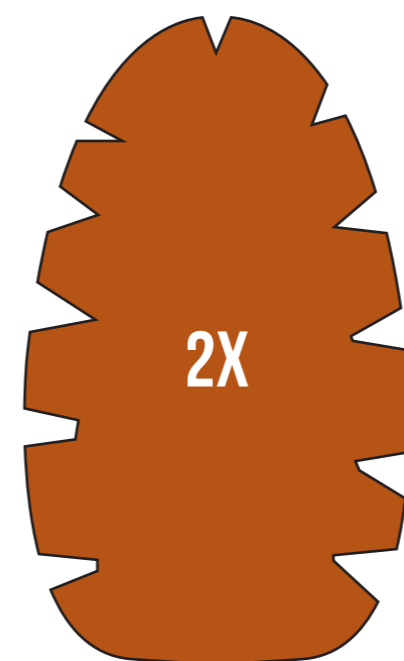
~
MOLDES



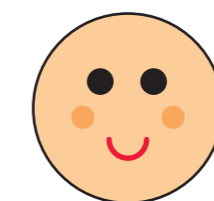
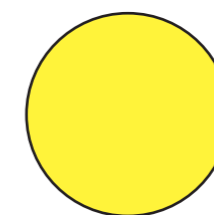
~
FRENTE



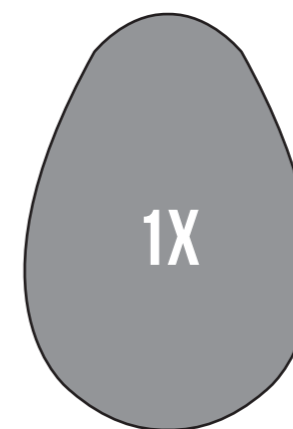
~
VERSO



2X

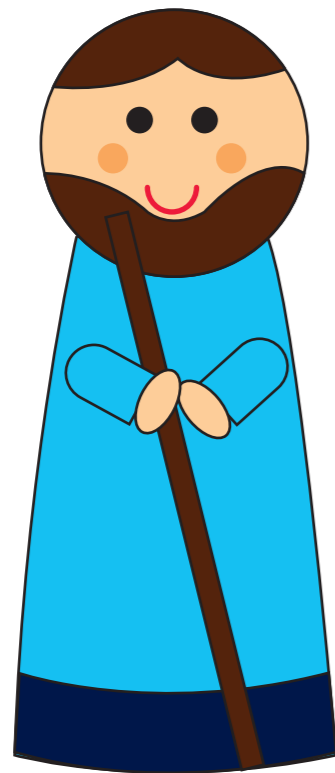


} 1X

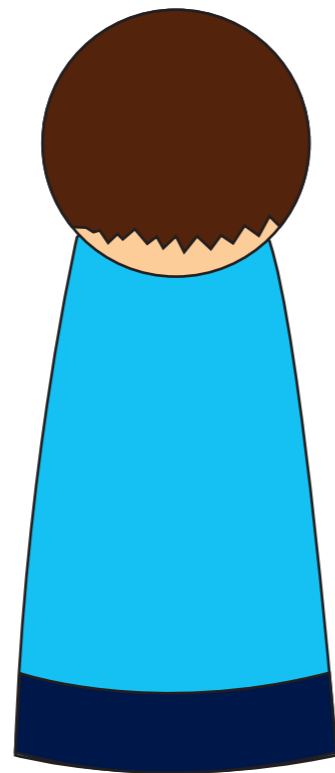


1X

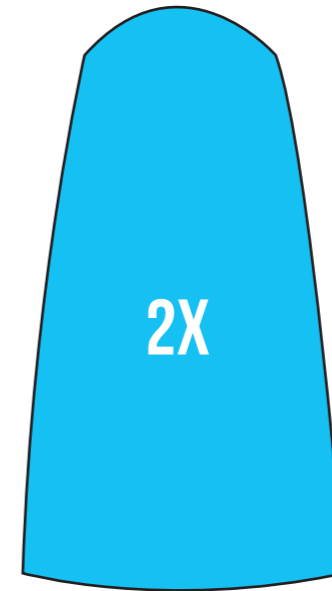
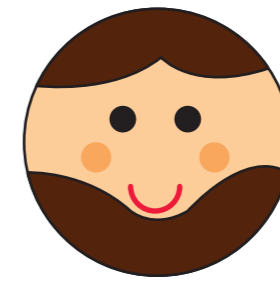
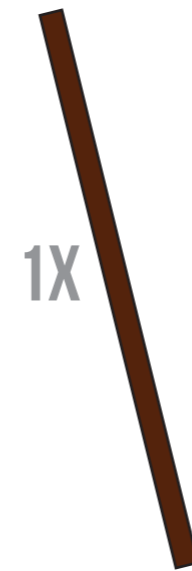
~
MOLDES



~
FRENTE



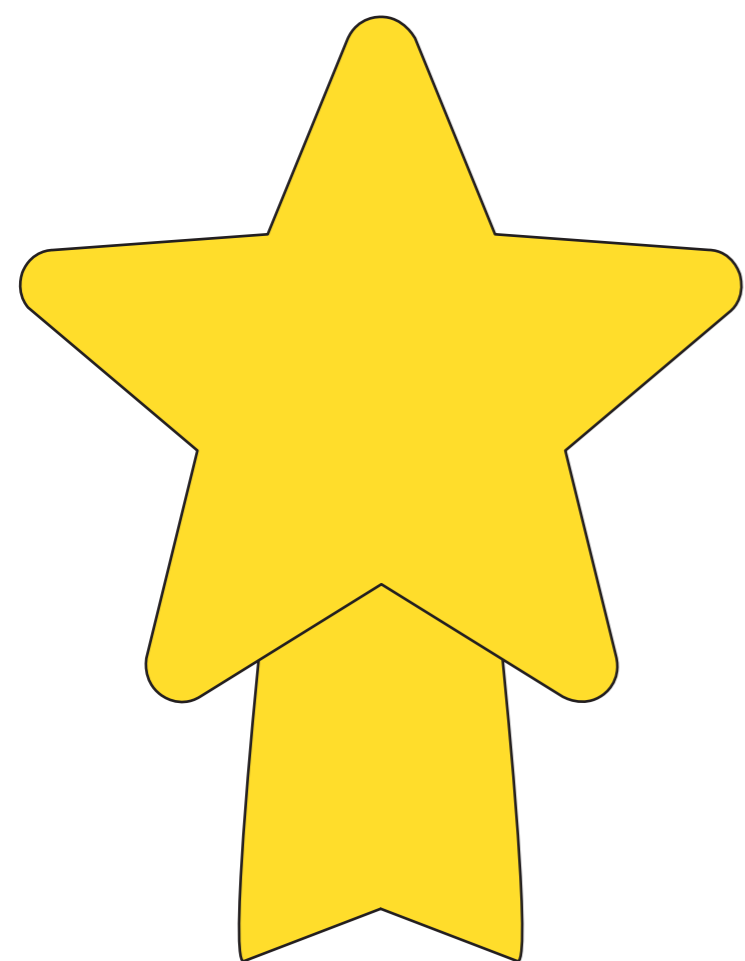
~
VERSO



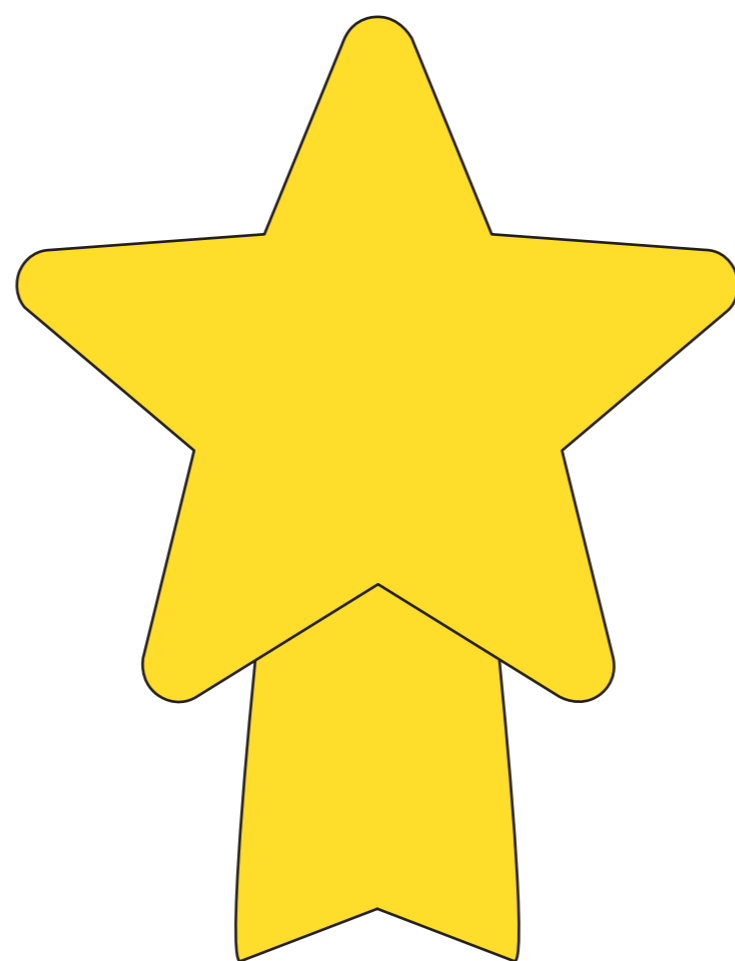
} 1X

+ } 2X

~
MOLDES



~
FRENTE



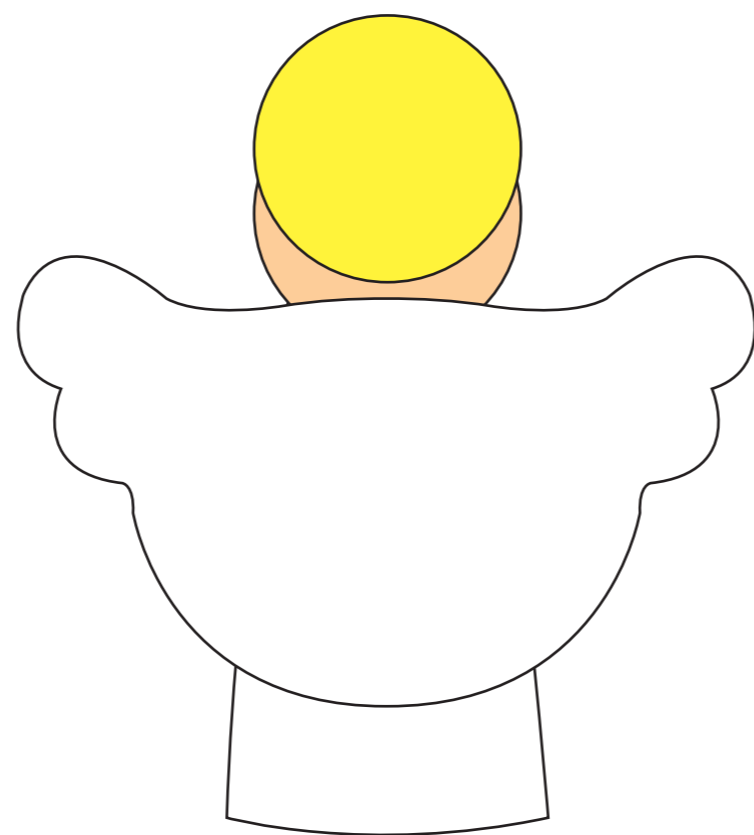
~
VERSO



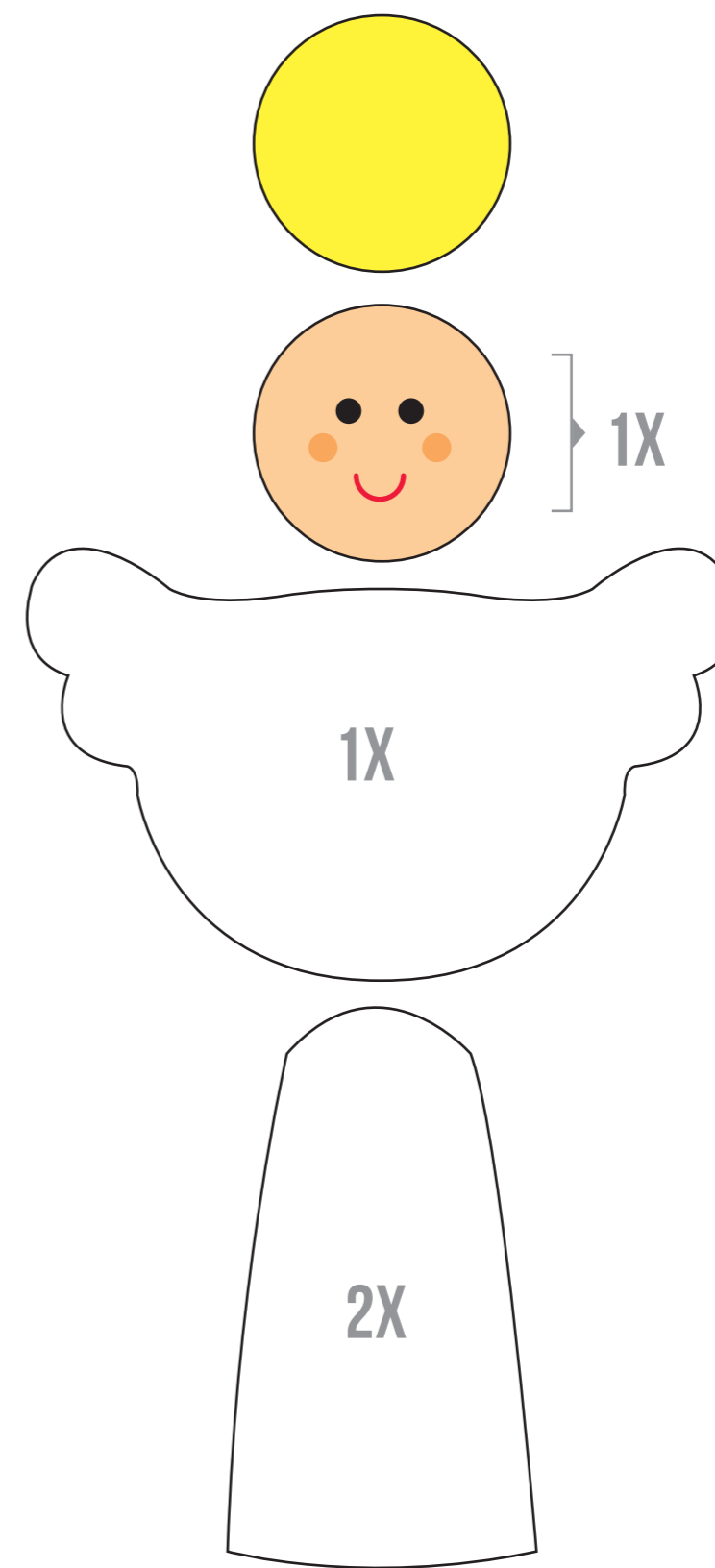
~
MOLDES



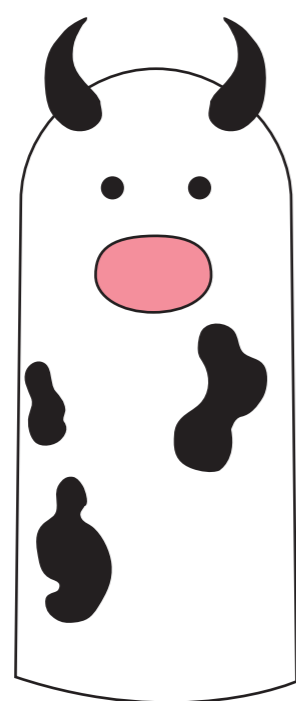
~
FRENTE



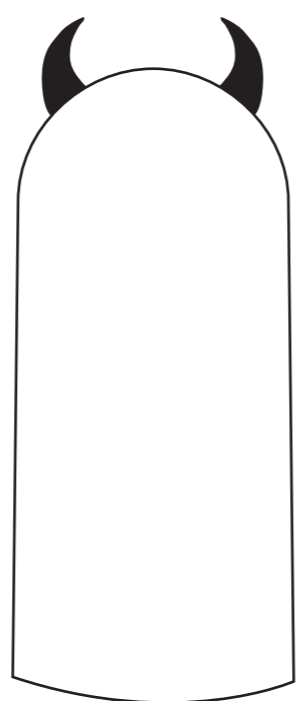
~
VERSO



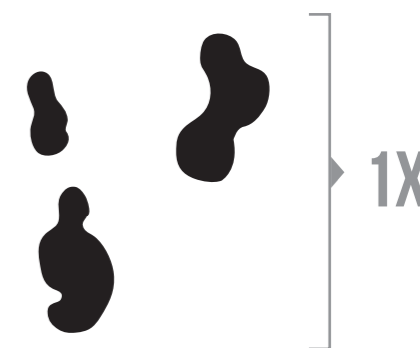
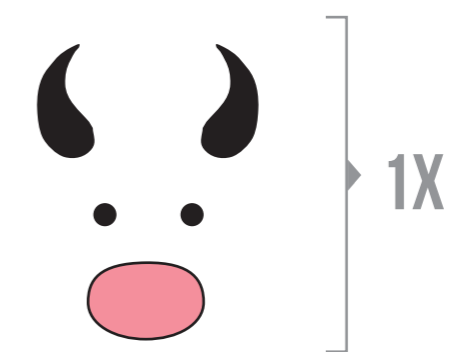
~
MOLDES



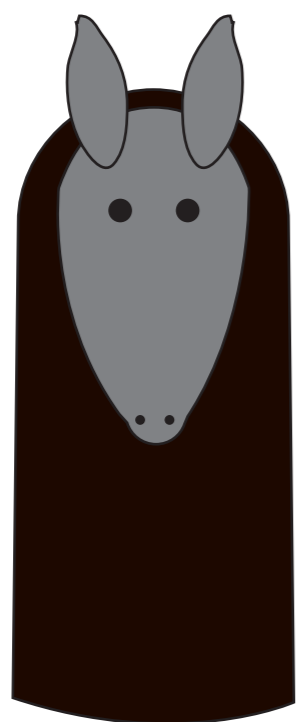
~
FRENTE



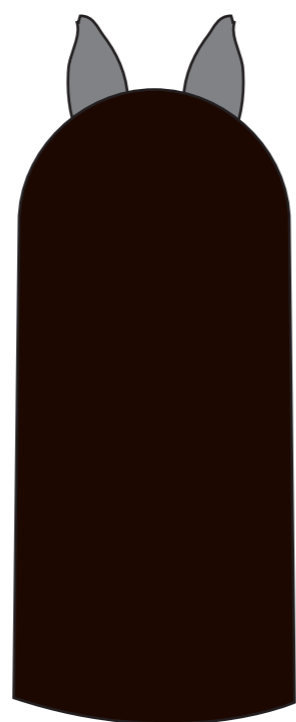
~
VERSO



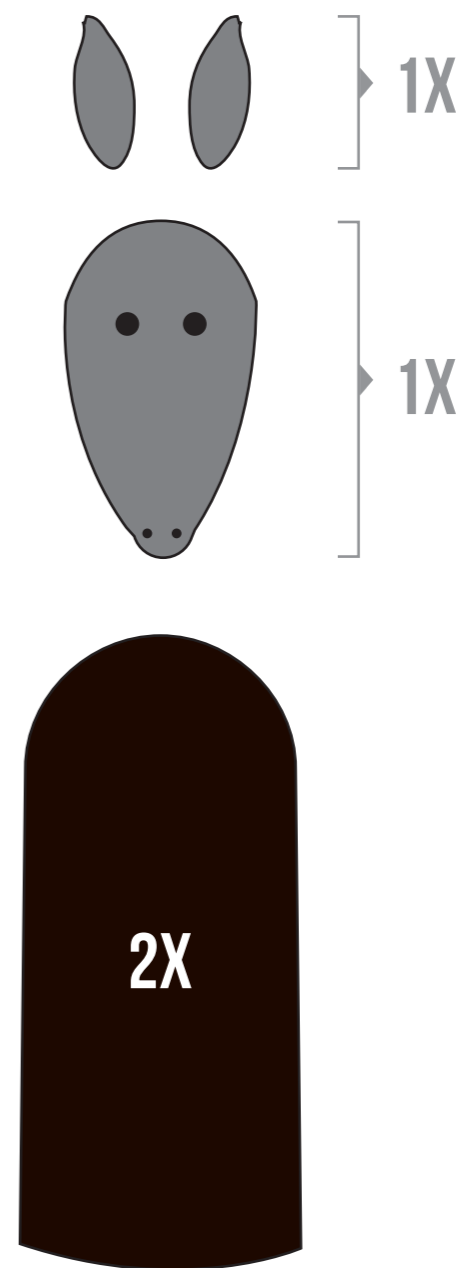
~
MOLDES



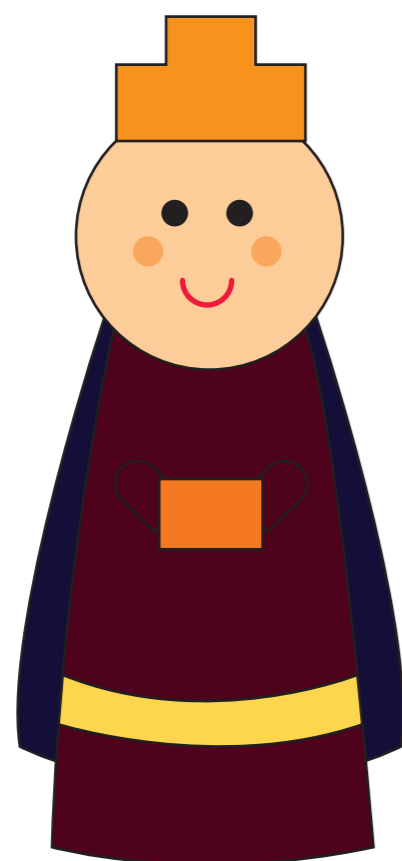
~
FRENTE



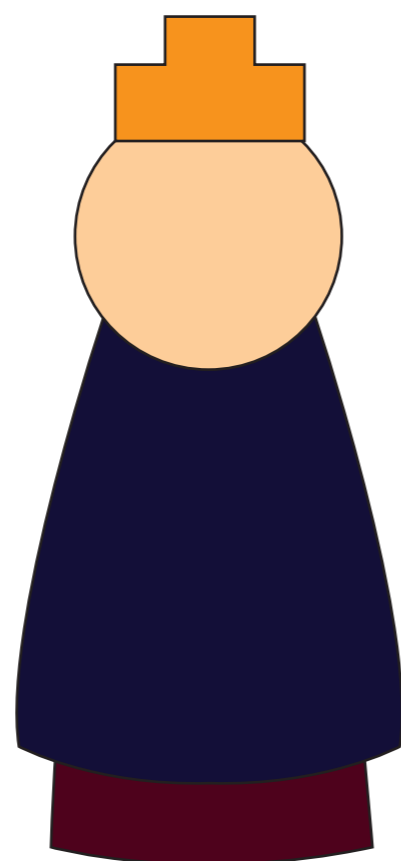
~
VERSO



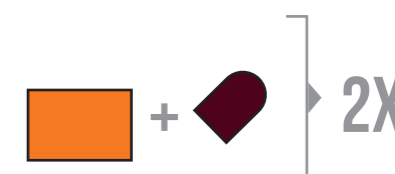
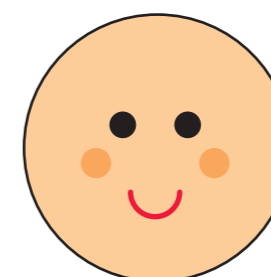
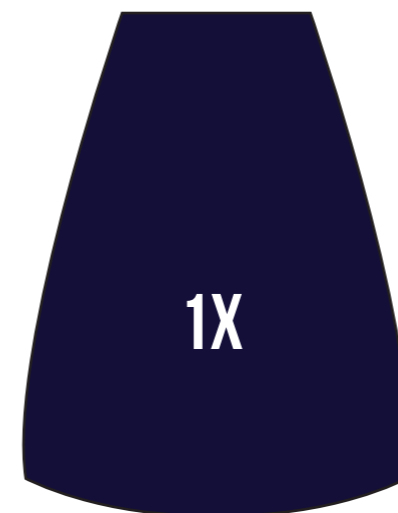
~
MOLDES



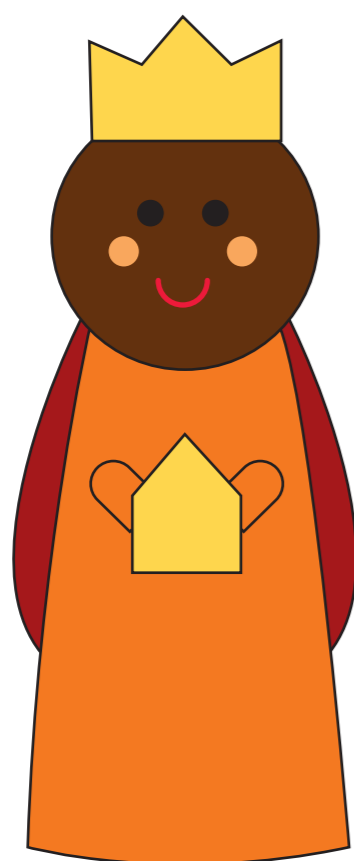
~
FRENTE



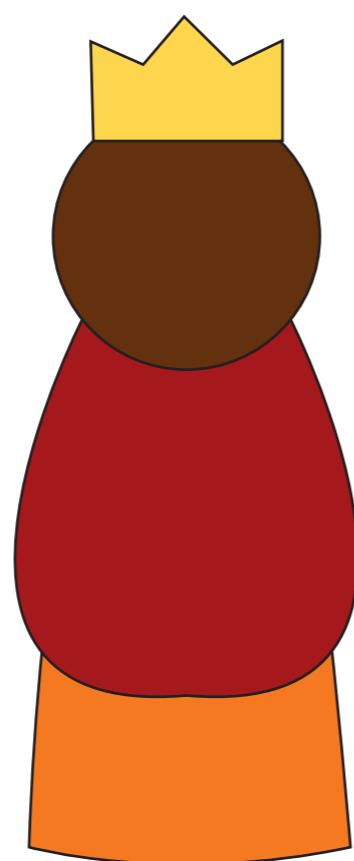
~
VERSO



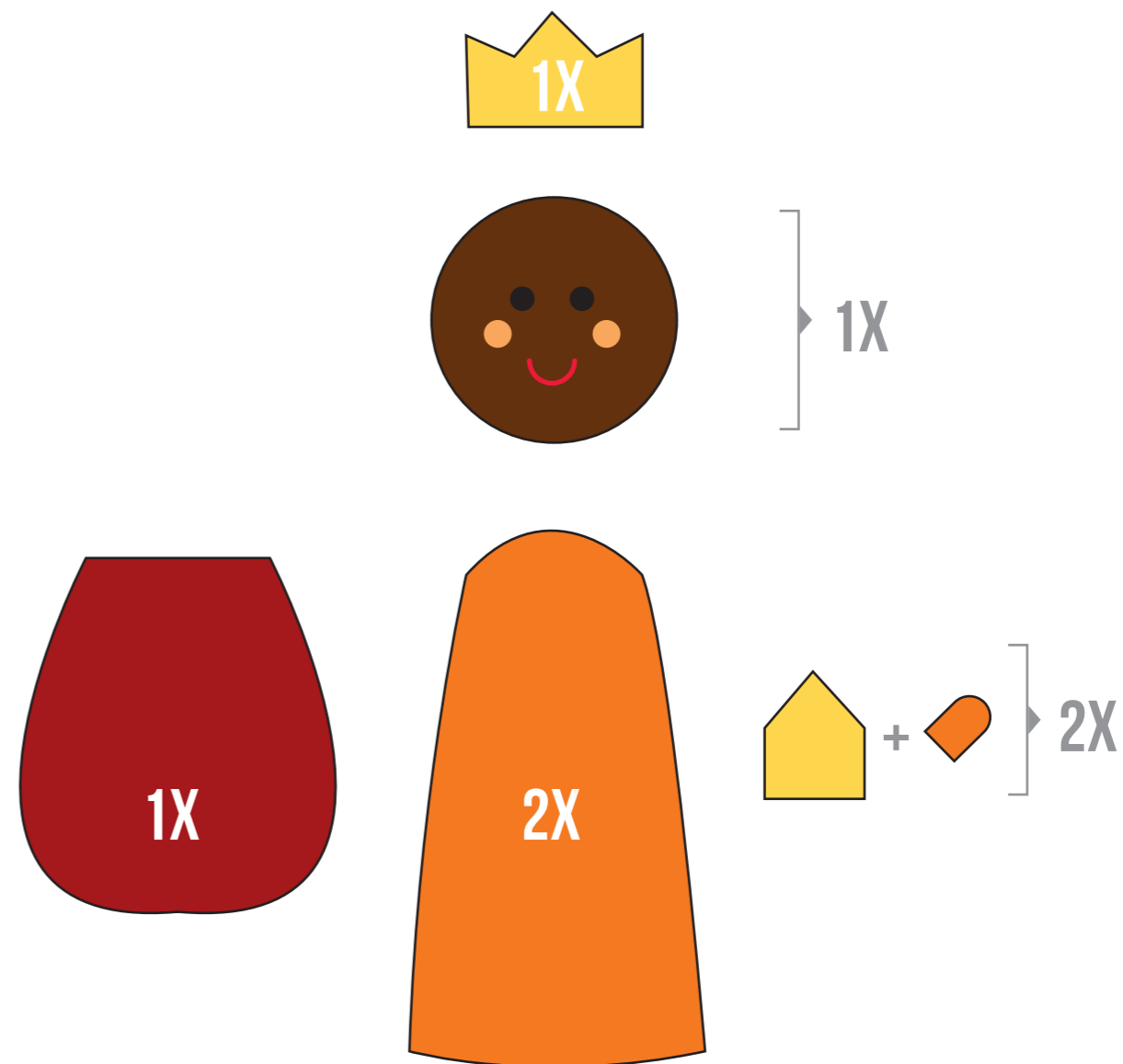
~
MOLDES



~
FRENTE



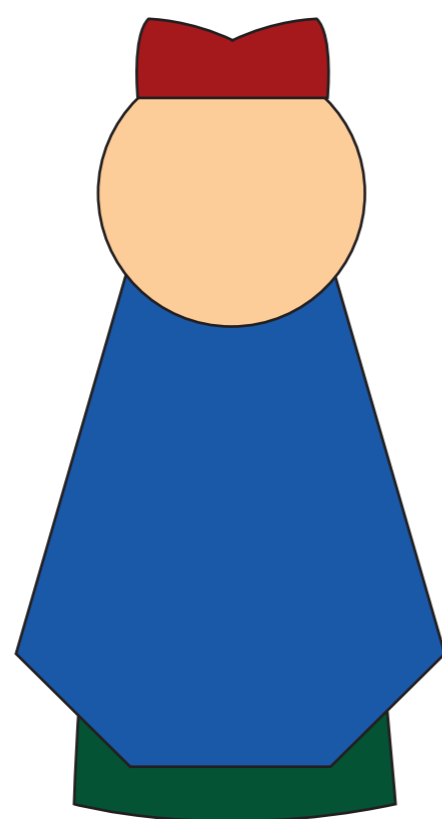
~
VERSO



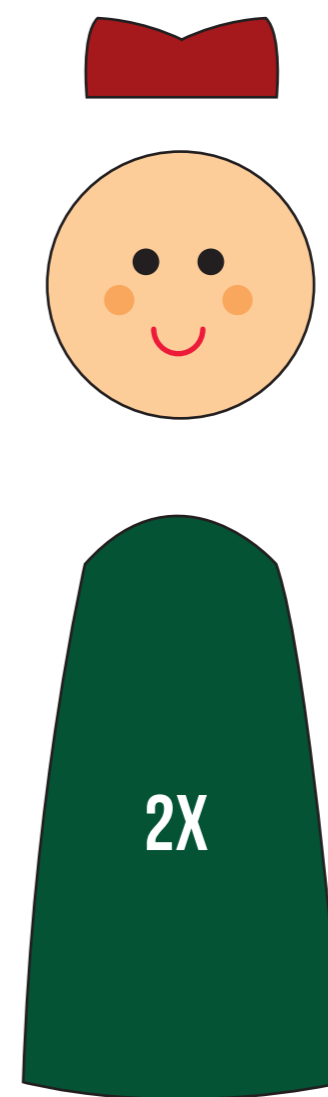
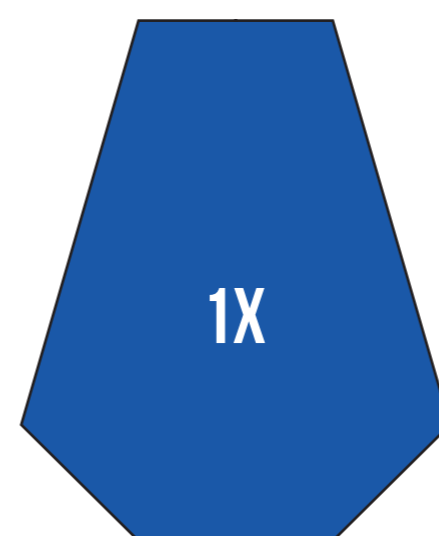
~
MOLDES



~
FRENTE



~
VERSO



~
MOLDES



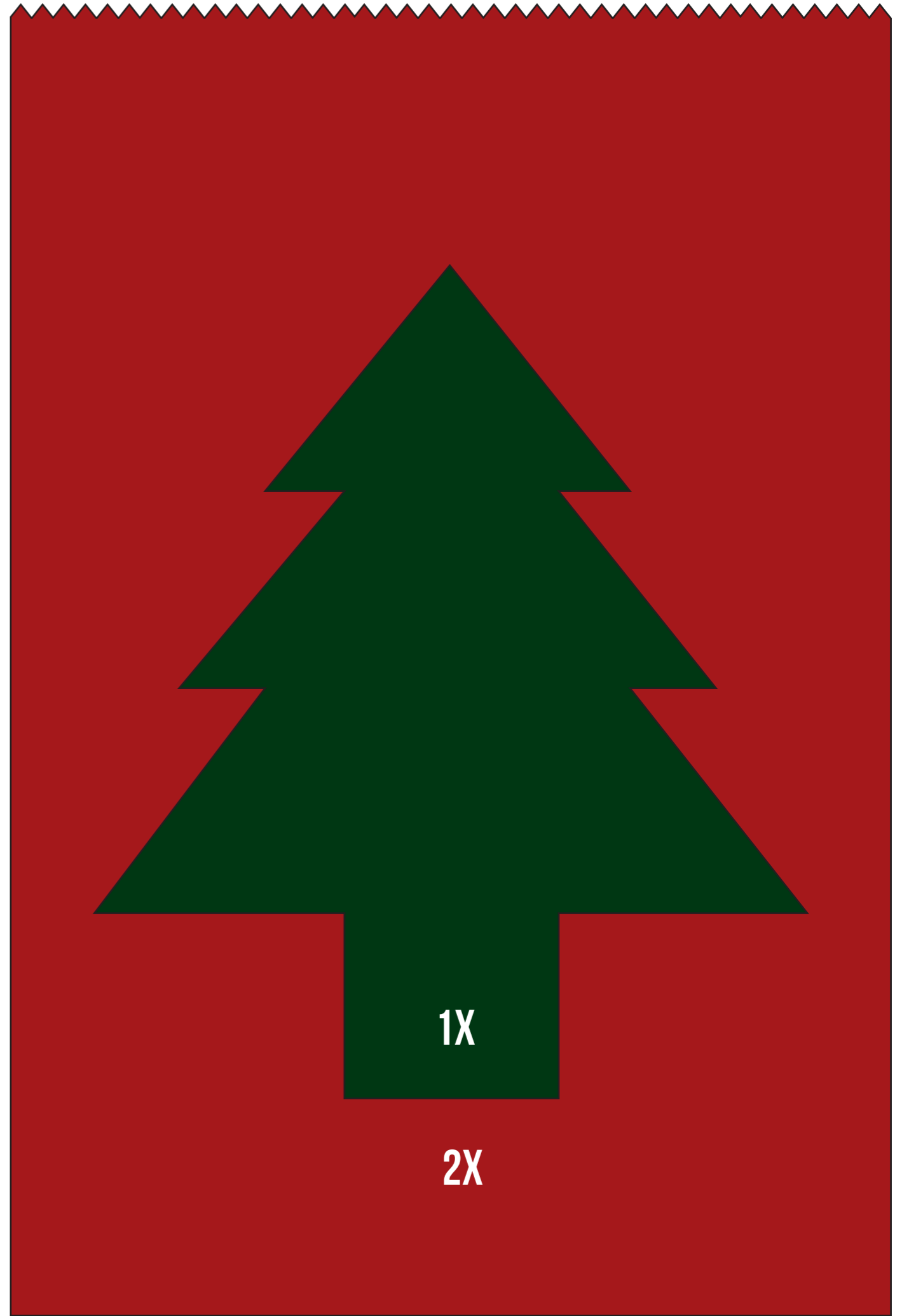
} 1X

} 2X

} 1X



~
FRENTE



1X

2X

~
MOLDES

